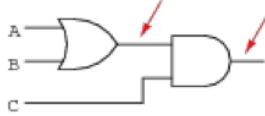


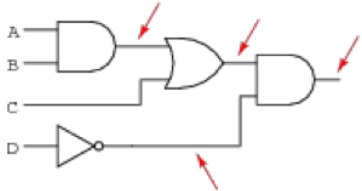
### Question 1:

Convert the following logic gate circuit into a **Boolean** expression, writing **Boolean** sub-expressions next to each gate output in the diagram:



### Question 2:

Convert the following logic gate circuit into a **Boolean** expression, writing **Boolean** sub-expressions next to each gate output in the diagram:



### Question 3:

Convert the following logic gate circuit into a **Boolean** expression, writing **Boolean** sub-expressions next to each gate output in the diagram:

